# INTEGRATED CIRCUITS



Product data Supersedes data of 2001 Jul 12 File under Integrated Circuits, Standard Analog 2001 Aug 27



### SA57000-XX

#### **GENERAL DESCRIPTION**

The CapFREE<sup>™</sup> SA57000 is the first in a new family of unique low dropout regulators. It needs no external capacitors, offers a low output noise voltage of 30 µV<sub>RMS</sub>, and an ultra-low dropout voltage of 55 mV @ 50 mA output current. To accommodate high density layouts, it is packaged in the small footprint 5 leaded SOT23-5 (SO5). It is ideal for all portable and cellular phone applications.

Additional features include power and thermal shutdown, output current limitation, power OK status, thermal warning, and external logic-controlled on-off via the PWRON pin.

#### **FEATURES**

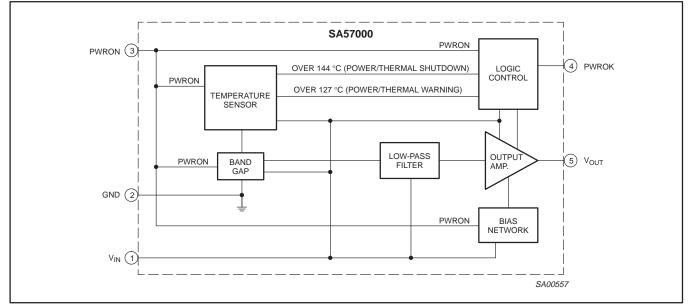
- CapFREE: No output capacitor needed, stable for all capacitive loads, regardless of ESR
- Low 30  $\mu$ V<sub>RMS</sub> noise without noise bypass capacitor
- Preset output voltages to 2.5 V, 2.8 V, 3.0 V, 3.3 V and 3.6 V; other voltages available upon request. 2% output voltage accuracy
- 150 mA maximum output current with current limitation
- Typical dropout voltage 55 mV @ 50 mA output current
- 85 μA typical ground current
- Thermal-overload and short-circuit protection
- PWROK pin: both power status and thermal warning indicator
- PWRON pin offers logic-controlled shutdown
- Maximum line regulation: 0.1%/V
- Maximum load regulation: 0.02%/mA.

SIMPLIFIED SYSTEM DIAGRAM



#### APPLICATIONS

- Cordless and mobile phones
- Industrial and medical equipment
- Other battery-powered equipment.



#### Figure 1. Simplified system diagram.

CapFREE is a trademark of Philips Electronics North America Corporation.

# SA57000-XX

#### **ORDERING INFORMATION**

TYPE NUMBER	PACKAGE	TEMPERATURE			
TTPE NOMBER	NAME	DESCRIPTION	VERSION	RANGE	
SA57000- <b>XX</b> D	SOT23-5, SOT25, SO5	plastic small outline package; 5 leads (see dimensional drawing)	SOT680-1	–40 to +85 °C	

#### NOTE:

The device has five voltage output options, indicated by the XX on the Type Number.

ХХ	VOLTAGE (Typical)
25	2.5 V
28	2.8 V
30	3.0 V
33	3.3 V
36	3.6 V

#### Part number marking

Each package is marked with a four letter code. The first three letters designate the product. The fourth letter, represented by 'x', is a date tracking code.

Part number	Marking
SA57000-25D	АВКх
SA57000-28D	ABLx
SA57000-30D	A B M x
SA57000-33D	ABNx
SA57000-36D	ABPx

#### **PIN CONFIGURATION**

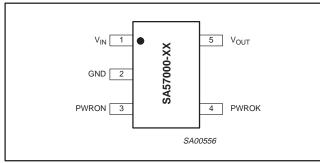


Figure 2. Pin configuration.

#### **PIN DESCRIPTION**

PIN	SYMBOL	DESCRIPTION	
1	V <sub>IN</sub>	Regulator input. $V_{OUT(nom)}$ + 0.1 V to 5.5 V. No bypass capacitor required.	
2	GND	Ground.	
3	PWRON	Power-on input. Active-HIGH. A logic LOW powers down regulator. The shutdown quiescent current is typically 50 nA. Connect to V <sub>IN</sub> for manual operation.	
4	PWROK	Power OK indicator, including thermal warning. Trips (goes LOW) at 127 °C (±2°), <b>or</b> when power falls typically 6% below V <sub>OUT(nom)</sub> .	
5	V <sub>OUT</sub>	Regulator output. Sources up to 150 mA. No bypass capacitors required.	

### MAXIMUM RATINGS

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V <sub>IN</sub>	V <sub>IN</sub> to GND voltage	-0.3	5.5	V <sub>dc</sub>
V <sub>PWRON</sub>	PWRON to GND voltage	-0.3	5.5	V <sub>dc</sub>
V <sub>OUT</sub>	OUT to GND voltage	-0.3	V <sub>IN</sub> + 0.3	V <sub>dc</sub>
T <sub>amb</sub>	Operating ambient temperature	-40	+85	°C
Т <sub>ј</sub>	Junction temperature	-	+150	°C
T <sub>stg</sub>	Storage temperature	-65	+160	°C
Р	Power dissipation	-	575	mW
R <sub>th(j-a)</sub>	Thermal resistance from junction to ambient	-	140	°C/W

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Product data

### **CHARACTERISTICS**

 $V_{IN} = V_{OUT(nom)} + 0.5$  V. (Note 1.)

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>IN</sub>	input voltage		V <sub>OUT(nom)</sub>	_	5.5	V
	output voltage accuracy <sup>2</sup>	I <sub>OUT</sub> = 1 mA				
		$T_{amb} = +25 \ ^{\circ}C$	-	±1	-	%
		–40 °C ≤ T <sub>amb</sub> ≤ +85 °C	-2.0	-	2.0	%
I <sub>LIM</sub>	current limit		160	300	-	mA
l <sub>Q</sub>	ground pin current	I <sub>OUT</sub> = 1 mA to 150 mA	-	85	150	μΑ
	dropout voltage <sup>3</sup>	I <sub>OUT</sub> = 1 mA	-	1	-	mV
		I <sub>OUT</sub> = 50 mA	-	55	120	mV
		I <sub>OUT</sub> = 150 mA	-	165	-	mV
$\Delta V_{LNR}$	line regulation	$V_{IN} = (V_{OUT} + 0.1 \text{ V}) \text{ to } 5.5 \text{ V}; I_{OUT} = 20 \text{ mA}$	-	-	0.1	%/V
$\Delta V_{LDR}$	load regulation	I <sub>OUT</sub> = 1 mA to 150 mA	-	0.01	0.02	%/mA
e <sub>n</sub>	output voltage noise	f = 10 Hz to 100 kHz, $C_{OUT}$ = 10 $\mu$ F	-	30	-	μV <sub>RMS</sub>
Shutdown	-	-				
V <sub>IH</sub>	PWRON input threshold (HIGH ON-state)	$V_{\text{IN}} \rightarrow V_{\text{OUT(nom)}} \rightarrow 5.5 \text{ V}$	$0.7  imes V_{IN}$	_	-	V
V <sub>IL</sub>	PWRON input threshold (HIGH ON-state)	$V_{\text{IN}} \rightarrow V_{\text{OUT(nom)}} \rightarrow 5.5 \text{ V}$	-	_	$0.3  imes V_{IN}$	V
IPWRON	PWRON input bias current	V <sub>PWRON</sub> = V <sub>IN</sub>				
		$T_{amb} = +25 \ ^{\circ}C$	-	0.01	1	μΑ
		T <sub>amb</sub> = +85 °C	-	0.05	-	μΑ
I <sub>Q(SHDN)</sub>	shutdown supply current	V <sub>OUT</sub> = 0 V				
		$T_{amb} = +25 \circ C$	-	0.05	1	μΑ
		T <sub>amb</sub> = +85 °C	-	0.2	1	μΑ
t <sub>PWRON</sub>	power-on start-up time <sup>4</sup>	$I_{OUT} = 1 \text{ mA}, C_{OUT} = 100 \text{ nF}$				
		$T_{amb} = +25 \text{ °C}$	-	25	100	μs
These also	hate the start (Nets 2)	$T_{amb} = -40$ to +85 °C	-	35	200	μs
-	rotection (Note 2)					
T <sub>SHDN</sub>	thermal shut-down temperature		-	144	-	°C
ΔT <sub>SHDN</sub>	thermal shut-down hysteresis		-	13	-	°C
PWROK ou	Itput (power and temperature OK	.) (Note 2)				
	PWROK trip temperature		-	127	-	°C
	PWROK trip temperature hysteresis		-	12	-	°C
	PWROK trip as percentage of $V_{OUT(nom)}$		-3.5	-6	-8	%
	PWROK hysteresis as percentage of V <sub>OUT(nom)</sub>		-	2	-	%
	PWROK output (when tripped)	I <sub>SINK</sub> = 0.5 mA	_	0.1	0.4	V

#### NOTES:

1. Limits are production tested at Tamb = +25 °C. All devices are 100% production tested at 25 °C. Limits over the operating tempreature are 2. Accuracy ±2 °C over temperature range guaranteed by design and characterization.
3. The dropout voltage is defined as V<sub>IN</sub> - V<sub>OUT</sub> where V<sub>OUT</sub> is 100 mV below the value of V<sub>OUT</sub> for V<sub>IN</sub> = V<sub>OUT</sub> + 0.5 V.
4. Time needed for V<sub>OUT</sub> to reach 95% of V<sub>OUT(nom)</sub>.

### SA57000-XX

#### **TYPCIAL PERFORMANCE CURVES**

Measurements taken with the SA57000-33 (3.3 volt output).

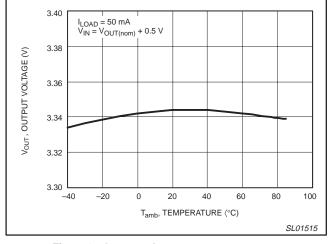


Figure 3. Output voltage versus temperature.

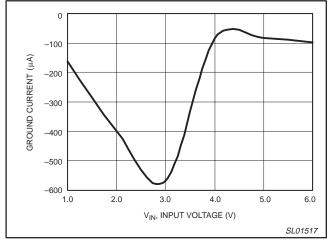


Figure 5. Ground current versus input voltage (no load).

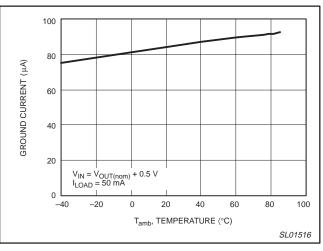


Figure 4. Ground current versus temperature.

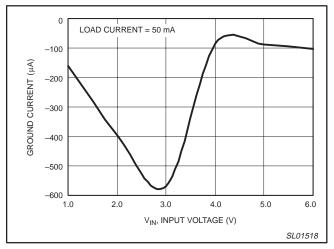


Figure 6. Ground current versus input voltage with load.

### SA57000-XX

#### **PACKING METHOD**

The SA57000-XX is packed in reels, as shown in Figure 7.

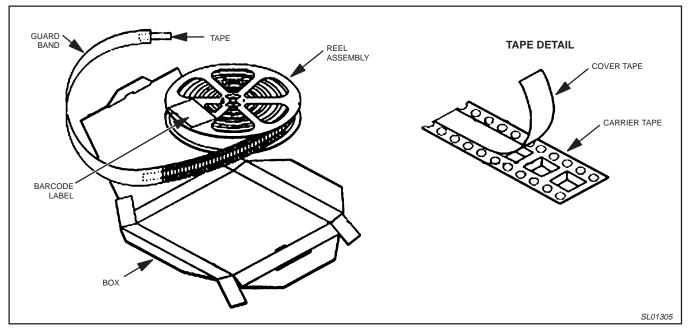
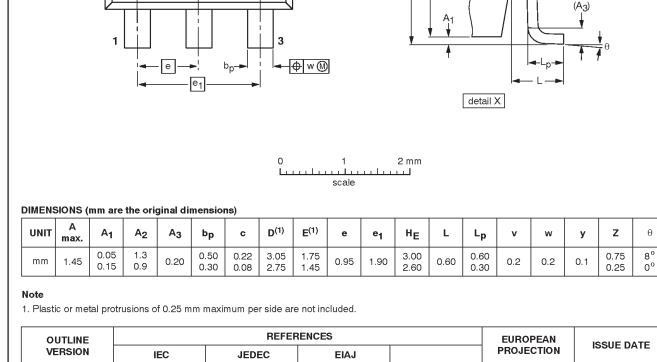
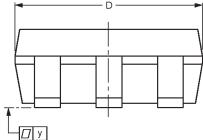


Figure 7. Tape and reel packing method

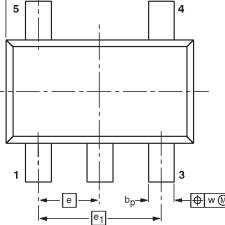
SOT680-1

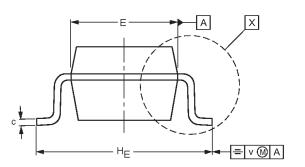


#### SO5: plastic small outline package; 5 leads; body width 1.6 mm



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SOT680-1

# SA57000-XX

MO-178

Product data

#### Data sheet status

Data sheet status <sup>[1]</sup>	Product status <sup>[2]</sup>	Definitions
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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